

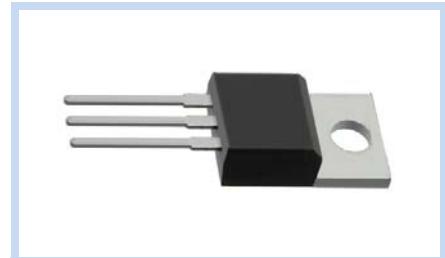
**N-Channel MOSFET  
500V 18A 208W TO-220**

MFT50N18T220

**MERITEK**

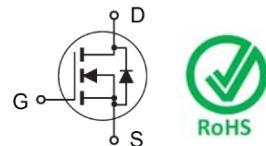
**FEATURE**

- $R_{DS(ON)} < 0.27\Omega$ ,  $V_{GS} = 10V$ ,  $I_D = 9A$
- High Power and Current Handling Capability
- Super High Dense Cell Design for Extremely Low  $R_{DS(ON)}$
- Lead free in compliance with EU RoHS 2.0



**MECHANICAL DATA**

- Case: TO-220 Package
- Terminals: Solderable per MIL-STD-750, Method 2026

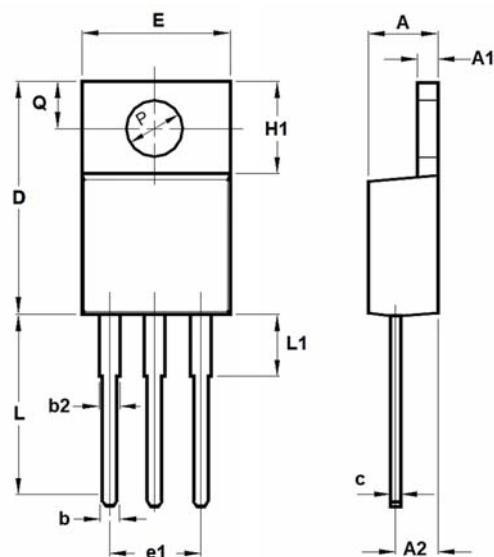


**MAXIMUM RATINGS**

Parameter		Symbol	Value	Unit
Drain-Source Voltage		$V_{DS}$	500	V
Gate-Source Voltage		$V_{GS}$	$\pm 30$	V
Drain Current – Continuous	$T_c = 25^\circ C$	$I_D$	18	A
	$T_c = 100^\circ C$		11	A
Drain Current – Pulsed	$T_c = 25^\circ C$	$I_{DM}$	72	A
Power Dissipation	$T_c = 25^\circ C$	$P_D$	208	W
	Derate above $25^\circ C$		1.6	W/ $^\circ C$
Single Pulsed Avalanche Energy		$E_{AS}$	859	mJ
Single Pulse Avalanche Current		$I_{AS}$	18	A
Thermal Resistance Junction to Ambient		$R_{\theta JA}$	62.5	$^\circ C/W$
Thermal Resistance Junction to Case		$R_{\theta JC}$	0.6	$^\circ C/W$
Operating Junction and Storage Temperature		$T_J, T_{STG}$	-55 to 150	$^\circ C$

**DIMENSIONS**

Item	Min. (mm)	Max. (mm)
A	4.320	4.826
A1	1.220	1.397
A2	2.032	2.921
b	0.610	0.910
b2	1.143	1.778
c	0.356	0.530
D	14.224	16.510
E	9.652	10.668
e1	5.080	5.080
H1	5.842	6.858
L	12.700	14.732
L1	3.400	4.000
Q	2.540	3.429



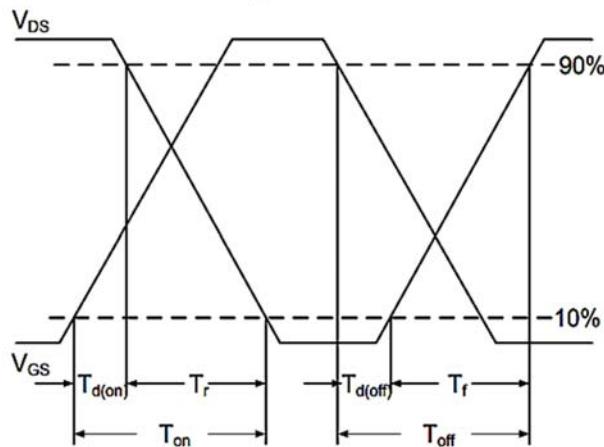
## ELECTRICAL CHARACTERISTICS

Off Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Drain-Source Breakdown Voltage</b>	$V_{GS}=0V, I_D=250\mu A$	$BV_{DSS}$	500	--	--	V
<b>Drain-Source Leakage Current</b>	$V_{DS}=500V, V_{GS}=0V$	$I_{DSS}$	--	--	1	$\mu A$
<b>Gate-Body Leakage Current, Forward</b>	$V_{GS}=30V, V_{DS}=0V$	$I_{GSSF}$	--	--	100	nA
<b>Gate-Body Leakage Current, Reverse</b>	$V_{GS}=-30V, V_{DS}=0V$	$I_{GSSR}$	--	--	-100	nA
On Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Static Drain-Source On-Resistance</b>	$V_{GS}=10V, I_D=9A$	$R_{DS(ON)}$	--	0.24	0.27	$\Omega$
<b>Gate Threshold Voltage</b>	$V_{GS}=V_{DS}, I_D=250\mu A$	$V_{GS(th)}$	2	--	4	V
Dynamic Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Total Gate Charge</b>	$V_{DS}=400V, V_{GS}=10V, I_D=18A$	$Q_g$	--	58	--	nC
<b>Gate-Source Charge</b>		$Q_{gs}$	--	11	--	nC
<b>Gate-Drain Charge</b>		$Q_{gd}$	--	23	--	nC
<b>Turn-On Delay Time</b>	$V_{DD}=250V, V_{GS}=10V, R_G=25\Omega$ $I_D=18A$	$T_{d(on)}$	--	36	--	ns
<b>Rise Time</b>		$T_r$	--	28	--	ns
<b>Turn-Off Delay Time</b>		$T_{d(off)}$	--	78	--	ns
<b>Fall Time</b>		$T_f$	--	11	--	ns
<b>Input Capacitance</b>	$V_{DS}=25V, V_{GS}=0V, F=1MHz$	$C_{iss}$	--	2465	--	pF
<b>Output Capacitance</b>		$C_{oss}$	--	300	--	pF
<b>Reverse Transfer Capacitance</b>		$C_{rss}$	--	10	--	pF
Drain-Source Body Diode	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Drain-Source Diode Forward Current</b>	--	$I_s$	--	--	18	A
<b>Diode Forward Voltage</b>	$V_{GS}=0V, I_s=18A, T_J=25^\circ C$	$V_{SD}$	--	--	1.4	V

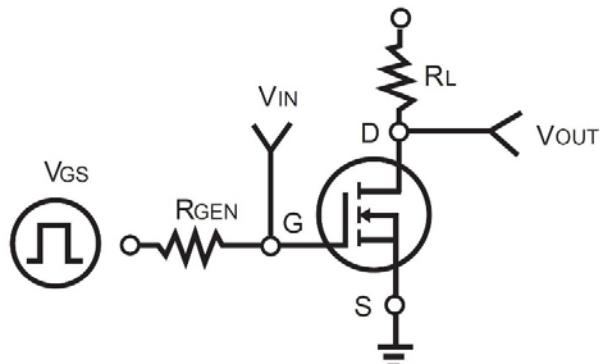
**Note:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$
3. Guaranteed by design, not subject to production testing.
4. Limited only by maximum temperature allowed
5. Pulse width limited by safe operating area
6. Full Package  $I_{S(max)}=10A$
7. Full Package  $V_{SD}$  test condition  $I_s=10A$
5. L= 5.3mH,  $I_{as}=18A$ ,  $V_{DD}=50V$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ C$

Switching Time Waveform



Switching Test Circuit



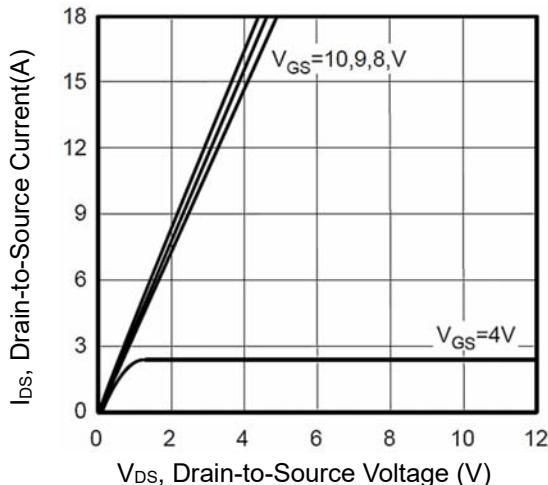
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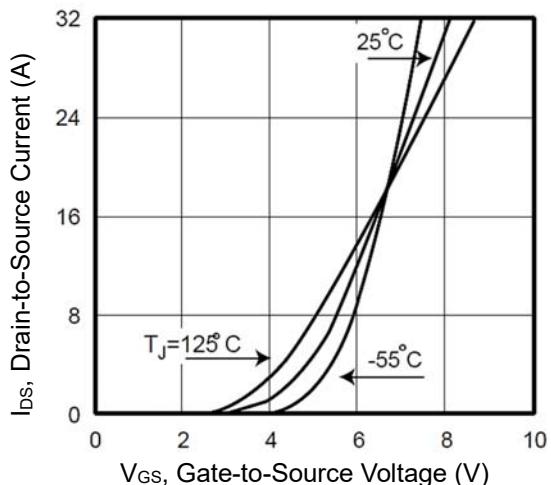
**MERITEK**

## CHARACTERISTIC CURVES

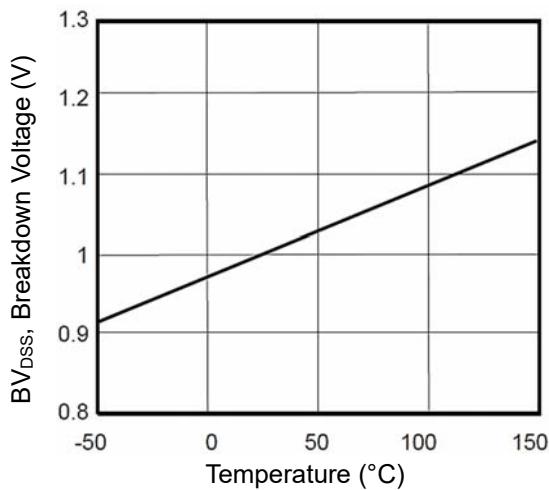
Output Characteristics



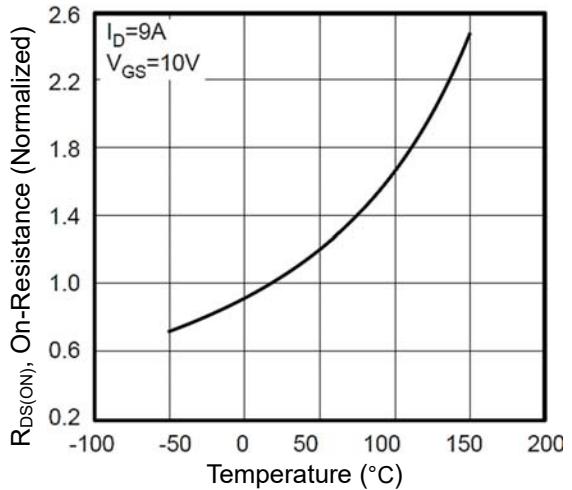
Transfer Characteristics



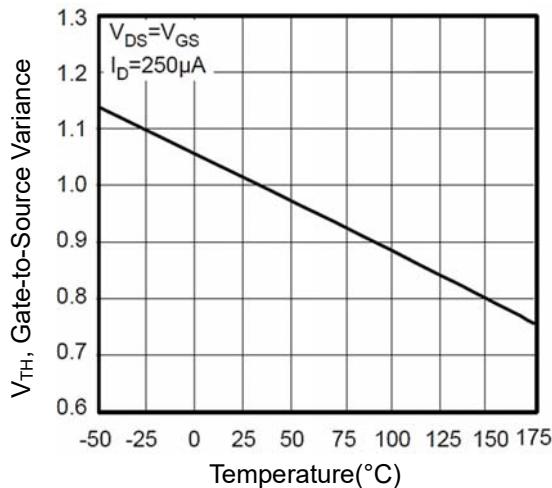
Breakdown Voltage vs. Temperature



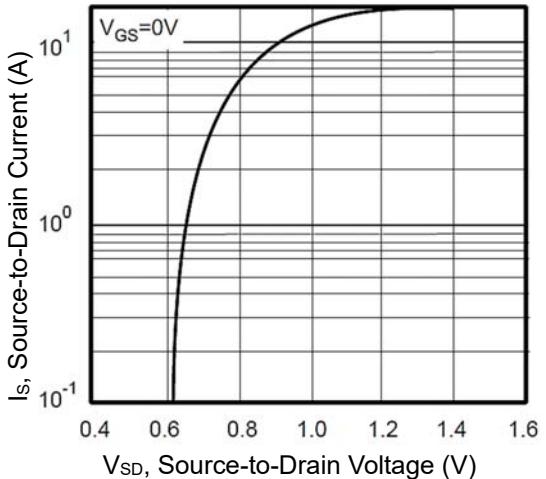
On-Resistance vs. Junction temperature



Threshold Voltage Variation with Temperature



Body Diode Characteristics



## CHARACTERISTIC CURVES

